

Test Report issued under the responsibility of:

NCB TÜV SÜD Product Service GmbH Ridlerstr. 65 D – 80339 München Germany

TEST REPORT IEC 62109-2

Safety of Power Converter for use in Photovoltaic Power Systems Part 2: Particular requirements for inverters

Report Number:	083-52008203-000 part 2 of 2
Date of issue:	2020-11-18
Total number of pages	28
CB Testing Laboratory:	CQC - Trusted(Jiangsu) Testing Technology Co., Ltd.
Address:	No.99, Wenlan Road, Xianlin University Zone, Xianlin Street, Qixia District, NanJing, China
Applicant's name:	Huawei Technologies Co., Ltd.
Address:	Administration Building Headquarters of Huawei Technologies Co., Ltd., Bantian, Longgang District, 518129 Shenzhen, PEOPLE'S REPUBLIC OF CHINA
Test specification:	
Standard:	IEC 62109-2:2011
Test procedure:	CB Scheme
Non-standard test method:	N/A
Test Report Form No:	IEC62109_28
Test Report Form(s) Originator:	LCIE - Laboratoire Central des Industries Electriques
Master TRF:	Dated 2016-08
Equipment and Components (IECEE :	
This publication may be reproduced in whole or in copyright owner and source of the material. IECER from the reader's interpretation of the reproduced	part for non-commercial purposes as long as the IECEE is acknowledged as E takes no responsibility for and will not assume liability for damages resulting material due to its placement and context.
If this Test Report Form is used by non- CB Scheme procedure shall be removed	IECEE members, the IECEE/IEC logo and the reference to the d.
This report is not valid as a CB Test R and appended to a CB Test Certificate	eport unless signed by an approved CB Testing Laboratory issued by an NCB in accordance with IECEE 02.
General disclaimer:	
The test results presented in this report re This report shall not be reproduced, exce	elate only to the object tested.

This report shall not be reproduced, except in full, without the written approval of the Issuing CB Testing Laboratory. The authenticity of this Test Report and its contents can be verified by contacting the NCB, responsible for this Test Report.

TRF No. IEC62109_2B

Page 2 of 28 Report No. 083-52008203-000 part 2 of 2

Test item description:	SOLAF	RINVERTER	
Trade Mark:	HUAWI	ĒI	
Manufacturer	Huawe	i Technologies Co	., Ltd.
	Ltd., B	istration Building H antian, Longgang I BLIC OF CHINA	leadquarters of Huawei Technologies Co., District, 518129 Shenzhen, PEOPLE'S
Model/Type reference:	SUN20 M3, SU	000-23KTL-M3, SU	JN2000-17KTL-M3, SUN2000-20KTL-M3, JN2000-28KTL-M3, SUN2000-29.9KTL- 3, SUN2000-36KTL-M3, SUN2000- 2KTL-M3
Ratings:	See ra	ting labels on pag	es 4 to 6 of 083-52008203-000 part 1
Responsible Testing Laboratory (as a	pplical	ole), testing proce	edure and testing location(s):
CB Testing Laboratory:		CQC - Trusted(Ji	angsu) Testing Technology Co., Ltd.
Testing location/ address	:		oad, Xianlin University Zone, Xianlin rict, NanJing, China
Tested by (name, function, signature)) :	Renjie Cao	Layie Caro
Approved by (name, function, signate	ure):	Yong Chen	- Jozig Clum
Testing procedure: CTF Stage 1	:		
Testing location/ address	:		
Tested by (name, function, signature)) :		
Approved by (name, function, signate	ure):		
Testing procedure: CTF Stage 2	:		
Testing location/ address			
Tested by (name + signature)			
Witnessed by (name, function, signal	ture).:		
Approved by (name, function, signate	ure):		
Testing procedure: CTF Stage 3	:		
Testing procedure: CTF Stage 4	:		
Testing location/ address	:		
Tested by (name, function, signature) :	a New R	
Witnessed by (name, function, signat	ture).:		
Approved by (name, function, signate	ure):	×	
and the second se			

Page 3 of 28 Report No. 083-52008203-000 part 2 of 2

List of Attachme	nts (including a total number of	pages in each attachment):	
	contain 2 parts listed in below tabl	,	
Item	Description		Pages
Part 1	IEC 62109-1(ed.1) test report		96
Part 2	IEC 62109-2(ed.1) test report	IEC 62109-2(ed.1) test report	
Summary of test	ing:		<u> </u>
All the tests resul	ts are confirmed to the requirement	ts of the standard.	
Tests performed clause):	(name of test and test	Testing location:	
on representative and additional fau SUN2000-40KTL Sault-tolerance 4.4.4.15.1; Sault-tolerance means – 4.4.4.15 Cooling system 4.4.4.17; Array insulation inverters for ungre grounded arrays Array residual Neverter backfor	e of residual current monitoring – e of automatic disconnecting .2; n failure – Blanketing test – n resistance detection for bunded and functionally	CQC - Trusted(Jiangsu) Testing Teo Ltd. No.99, Wenlan Road, Xianlin Univer Xianlin Street, Qixia District, NanJing	sity Zone,
Summary of con	pliance with National Difference	es (List of countries addressed):	
All tests were car	ried out according to IEC 62109-20	(ed.1).	

The text of IEC 62109-2:2011 was approved by CENELEC as a European Standard without any modification. Also, compliance with EN 62109-2:2011.

Copy of marking plate:

Please refer to pages 4 to 6 of report 083-52008203-000 part 1.

Page 4 of 28 Report No. 083-52008203-000 part 2 of 2

Test item particulars:	
Equipment mobility:	movable hand-held stationary fixed transportable for building-in
Connection to the mains:	 ☐ pluggable equipment ☐ direct plug-in ☑ permanent connection ☐ for building-in
Enviromental category:	⊠ outdoor ☐ indoor ☐ indoor unconditional conditional
Over voltage category Mains:	
Over voltage category PV:	
Mains supply tolerance (%)	±10 %
Tested for power systems:	TN / TT / IT
Testing of phase-phase voltage (V)	380/400/440/480V
Class of equipment:	⊠ Class I □ Class II □ Class III □ Not classified
Mass of equipment (kg):	43kg
Pollution degree:	3(external environment), 2(internal environment)
IP protection class:	IP66
Possible test case verdicts:	
- test case does not apply to the test object:	N/A
- test object does meet the requirement:	P (Pass)
- test object does not meet the requirement:	F (Fail)
Testing:	
Date of receipt of test item:	2020-11-05
Date (s) of performance of tests:	2020-11-05 to 2020-11-16
General remarks:	
"(See Enclosure #)" refers to additional information ap "(See appended table)" refers to a table appended to the	
Throughout this report a 🖂 comma / 🗔 point is u	sed as the decimal separator.
Manufacturer's Declaration per sub-clause 4.2.5 of	IECEE 02:
The application for obtaining a CB Test Certificate includes more than one factory location and a declaration from the Manufacturer stating that the sample(s) submitted for evaluation is (are) representative of the products from each factory has been provided	⊠ Yes ☐ Not applicable
When differences exist; they shall be identified in t	he General product information section.

Page 5 of 28 Report No. 083-52008203-000 part 2 of 2

Name and address of factory (ies):

1) Huawei Machine Co., Ltd.

No. 2, New City Avenue, Songshan Lake Sci. & Tech. Industry Park, 523808 Dongguan, Guangdong, PEOPLE'S REPUBLIC OF CHINA 2) Dongguan Yang Tian Electronic Technology Co., Ltd. (i-Brights)

No.152, Luyuan Rd., Keyuancheng, Tangxia Town

523710 Dongguan City, Guangdong Province

PEOPLE'S REPUBLIC OF CHINA

3) DongGuan Fuyi Precision Industry Co.,Ltd.

Floor 1st-4th, Building 12, No.6, Songshui Road, Songmu Village, Weifeng Industrial City, Dalang Town 523770 Dongguan, Guangdong

PEOPLE'S REPUBLIC OF CHINA

4) Shenzhen Fugui Precision Industry Co., Ltd.

F8d District, Foxconn Science and Technology Industrial Park, East side of Min Qing Road

Longhua District, Longhua Subdistrict, Longhua District 518110 Shenzhen

PEOPLE'S REPUBLIC OF CHINA

General product information:

Please refer to pages 8 to 13 of report 083-52008203-000 part 1.

Page 6 of 28 Report No. 083-52008203-000 part 2 of 2

IEC 62109-2					
Clause	Requirement + Test		Result - Remark		Verdict

4	GENERAL TESTING REQUIREMENTS		Р
4.4.4	Single fault conditions to be applied		Р
4.4.4.15	Fault-tolerance of protection for grid- interactive inverters		Р
4.4.4.15.1	Fault-tolerance of residual current monitoring according to 4.8.3.5: the residual current monitoring system operates properly	External isolating transformer is required for in parallel operation with grid, but RCMU is necessary based on analysis when multiple inverters in parallel operation with connection to the same winding of external transformer.	Ρ
	a) The inverter ceases to operate		Р
	 Indicates a fault in accordance with §13.9 		Р
	- Disconnect from the mains		Р
	 not re-connect after any sequence of removing and reconnecting PV power 		Р
	 not re-connect after any sequence of removing and reconnecting AC power 		Р
	 not re-connect after any sequence of removing and reconnecting both PV and AC power 		Ρ
	b) The inverter continues to operate		Р
	the residual current monitoring system operates properly under single fault condition		Р
	 Indicates a fault in accordance with §13.9 		Р
	c) The inverter continues to operate regardless of loss of residual current monitoring functionality		Ρ
	not re-connect after any sequence of removing and reconnecting PV power		Р
	 not re-connect after any sequence of removing and reconnecting AC power 		Р
	 not re-connect after any sequence of removing and reconnecting both PV and AC power 		Р
	 Indicates a fault in accordance with §13.9 		Р
4.4.4.15.2	Fault-tolerance of automatic disconnecting means		Р
4.4.4.15.2 .1	The means provided for automatic disconnection of a grid-interactive inverter from the mains shall:		Ρ
	 disconnect all grounded current-carrying conductors from the mains 	Not allowed to be used in grounded current-carrying system.	N/A
	 disconnect all ungrounded current- carrying conductors from the mains 		Р
	- be such that with a single fault applied to		Р

Page 7 of 28 Report No. 083-52008203-000 part 2 of 2

	IEC 62	109-2	
Clause	Requirement + Test	Result - Remark	Verdict
	the disconnection means or to any other location in the inverter, at least basic insulation or simple separation is maintained between the PV array and the mains when the disconnecting means is intended to be in the open state.		
4.4.4.15.2 .2	Design of insulation or separation complies with requirements of 7.3.7 of Part 1: report here Part 1 comment and verdict.	Consider the Vmax of PV array, overvoltage category, pollution degree, impulse withstand voltage of 4772V, the minimum required cl.5,2mm of contacts for altitude≤4000m, the detail see report of Part 1	Ρ
4.4.4.15.2 .3	For non-isolated inverter, automatic checking of the isolation provided by a disconnect means after single fault.		Р
	If the check fail: - any still-functional disconnection means shall be left in the open position		Р
	 at least basic or simple separation shall be maintained between the PV input and the mains 		Р
	- the inverter shall not start operation		Р
	 the inverter shall indicate a fault in accordance with 13.9 		Р
4.4.4.16	A stand-alone inverter with a transfer switch to transfer AC loads from the mains or other AC bypass source to the inverter output:	Not stand-alone inverter	N/A
	- shall continue to operate normally		N/A
	 shall not present a risk of fire as the result of an out-of-phase transfer 		N/A
	 shall not present a risk of shock as the result of an out-of-phase transfer 		N/A
	- And having control preventing switching: components for malfunctioning		N/A
4.4.4.17	Cooling system failure – Blanketing test No hazards according to the criteria of sub- clause 4.4.3 of Part 1 shall result from blanketing the inverter This test is not required for inverters restricted to use only in closed electrical operating areas.		Р
	Test stop condition: time duration value or stabilized temperature:	Stabilize without external surface of the inverter exceed 90°C	Р
4.7	ELECTRICAL RATINGS TESTS	1	N/A
4.7.4	Stand-alone Inverter AC output voltage and free	quency	N/A
4.7.4.1	General		N/A
4.7.4.2	Steady state output voltage at nominal DC input The steady-state AC output voltage shall not be less than 90 % or more than 110 % of the	See appended test table 4.7.4 Steady state Inverter AC output voltage and frequency	N/A

Page 8 of 28 Report No. 083-52008203-000 part 2 of 2

	IEC 62	109-2	
Clause	Requirement + Test	Result - Remark	Verdict
	rated nominal voltage with the inverter		
	supplied with its nominal value of DC input		
	voltage.		
4.7.4.3	Steady state output voltage across the DC	See appended test table 4.7.4	N/A
	input range	Steady state Inverter AC output	
	The steady-state AC output voltage shall not	voltage and frequency	
	be less than 85 % or more than 110 % of the rated nominal voltage with the inverter		
	supplied with any value within the rated range		
	of DC input voltage.		
4.7.4.4	Load step response of the output voltage at	See appended test table 4.7.4	N/A
	nominal DC input	Steady state Inverter AC output	
	The AC output voltage shall not be less than	voltage and frequency	
	85 % or more than 110 % of the rated nominal		
	voltage for more than 1,5 s after application or		
4745	removal of a resistive load.		N1/A
4.7.4.5	Steady state output frequency The steady-state AC output frequency shall	See appended test table 4.7.4 Steady state Inverter AC output	N/A
	not vary from the nominal value by more than	voltage and frequency	
	+4 % or -6 %.	voltage and frequency	
4.7.5	Stand-alone inverter output voltage waveform		N/A
4.7.5.1	General		N/A
4.7.5.2	The AC output voltage waveform of a		N/A
	sinusoidal output stand-alone inverter shall		
	have a total harmonic distortion (THD) not		
	exceeding of 10 % and no individual harmonic		
4750	at a level exceeding 6 %.		N1/A
4.7.5.3 4.7.5.3.1	Non-sinusoidal output waveform requirements General		N/A N/A
4.7.5.3.1	The total harmonic distortion (THD) of the		N/A N/A
4.7.3.3.2	voltage waveform shall not exceed 40 %.		
4.7.5.3.3	The slope of the rising and falling edges of the		N/A
	positive and negative half-cycles of the		
	voltage waveform shall not exceed 10 V/µs		
	measured between the points at which the		
	waveform has a voltage of 10 % and 90 % of		
	the peak voltage for that half-cycle.		
4.7.5.3.4	The absolute value of the peak voltage of the		N/A
	positive and negative half-cycles of the		
	waveform shall not exceed 1,414 times 110 %		
	of the RMS value of the rated nominal AC output voltage.		
4.7.5.4	Information requirements for non-sinusoidal		N/A
	waveforms		
	The instructions provided with a stand-alone		
	inverter not complying with 4.7.5.2 shall		
	include the information in 5.3.2.6.		
4.7.5.5	Output voltage waveform requirements for inver		N/A
	For an inverter that is intended only for use with		
	following requirements may be used as an alter requirements in $4.75.2$ to $4.75.3$	native to the waveform	
	requirements in 4.7.5.2 to 4.7.5.3. The combination of the inverter and dedicated	See attached document:	N/A
	load shall be evaluated to ensure that the	4.7.5.5 Evaluation of inverter for	

Page 9 of 28 Report No. 083-52008203-000 part 2 of 2

	IEC 62109-2		
Clause	Requirement + Test	Result - Remark	Verdic
	output waveform does not cause any hazards in the load equipment and inverter, or cause the load equipment to fail to comply with the applicable product safety standards.	dedicated load	
	The inverter shall be marked with symbols 9 and 15 of Table C.1 of Part 1.		N/A
	The installation instructions provided with the inverter shall include the information in 5.3.2.13.		N/A
4.8	ADDITIONAL TESTS FOR GRID-INTERACTIV	E INVERTERS	Р
4.8.1	General requirements regarding inverter isolation and array grounding		P
	- Type of Array grounding supported:	Ungrounded array	Р
	- Inverter isolation	Transformer-less solar inverter, but required an isolating transformer between the MAINS and inverter	Ρ
4.8.2	Array insulation resistance detection for inverters for ungrounded and functionally grounded arrays	(See attached table)	Р
4.8.2.1	Array insulation resistance detection for inverters for ungrounded arrays	Minimum Insulation Resistance before connection to the MAINS: $1100V/30mA=36,7 k\Omega$	Р
	Inverter shall have means to measure DC insulation resistance from PV input (array) to ground before starting operation		Р
	Or Inverter shall be provided with instruction in accordance with 5.3.2.11.	The expected insulation resistance of the array to ground shall be calculated based on an array insulation resistance of 40 M Ω per m ² either known according to 61730, calculate the practice PV system resistance with the surface area of the parallel and series panels and the set value maybe adjusted with agreement of authority agency.	Ρ
	Measured DC insulation resistance:	36,7kΩ x 0,9=33 kΩ	Р
	Inverter measurement circuit shall be capable of detecting insulation resistance below the limit value R= Vmax/30mA under normal conditions		Р
	Inverter measurement circuit shall be capable of detecting insulation resistance below the limit value R= Vmax/30mA with ground fault in the PV array	First with one pole grounded fault occurred, following an insulation resistance below limit simulated, then allow the inverter to start, the inverter shall not connect to the mains.	Ρ
	Isolated inverters shall indicate a fault if the insulation resistance is less than the limit value		Ρ

Page 10 of 28 Report No. 083-52008203-000 part 2 of 2

	IEC 62	109-2	
Clause	Requirement + Test	Result - Remark	Verdict
	Isolated inverter fault indication maintained until insulation resistance has recovered to a value higher than the limit value		Р
	Non-isolated inverters, or inverters with isolation current limits in the minimum inverter isolation re-		N/A
	 shall indicate a fault in accordance with 13.9 		N/A
	- shall not connect to the mains		N/A
4.8.2.2	Array insulation resistance detection for inverters for functionally grounded arrays	Not for functionally grounded arrays	N/A
	a-1)The value of the total resistance, including the intentional resistance for array functional grounding, the expected insulation resistance of the array to ground, and the resistance of any other networks connected to ground (for example measurement networks) must not be lower than R = (VMAX PV/30 mA) ohms.		N/A
	a-2) The installation instructions shall include the information required in 5.3.2.12.		N/A
	b-1) As an alternative to a), or if a resistor value lower than in a) is used, the inverter shall incorporate means to detect, during operation, if the total current through the resistor and any networks (for example measurement networks) in parallel with it, exceeds the residual current values and times in Table 31		N/A
	b-2) Inverter shall either disconnect the resistor or limit the current by other		N/A
	means: b-3) If the inverter is a non-isolated inverter, or has isolation not complying with the leakage current limits in the minimum inverter isolation requirements in Table 30, it shall also disconnect from the mains.		N/A
	 c) The inverter shall have means to measure the DC insulation resistance from the PV input to ground before starting operation, in accordance with 4.8.2.1. 		N/A
4.8.3	Array residual current detection		Р
4.8.3.1	General		Р
4.8.3.2	30 mA touch current type test for isolated inverters	See appended test table 4.8.3.2	Р
4.8.3.3	Fire hazard residual current type test for isolated inverters	See appended test table 4.8.3.3	Р
4.8.3.4	Protection by application of RCD's		N/A
_	- The requirement for additional protection in 4.8.3.1 can be met by provision of an RCD with a residual current setting of 30 mA, located between the inverter and the mains		N/A

Page 11 of 28 Report No. 083-52008203-000 part 2 of 2

	IEC 62	109-2	
Clause	Requirement + Test	Result - Remark	Verdict
	- The selection of the RCD type to ensure compatibility with the inverter must be made according to rules for RCD selection in Part 1.		N/A
	 The RCD provided integral to the inverter, or 		N/A
	- The RDC provided by the installer if details of the rating, type, and location for the RCD are given in the installation instructions per 5.3.2.9.		N/A
4.8.3.5	Protection by residual current monitoring	RCM is provided integrated in inverter	Р
4.8.3.5.1	General		Р
	Where required by Table 30, the inverter shall provide residual current monitoring that functions whenever the inverter is connected to the mains with the automatic disconnection means closed.		Р
	The residual current monitoring means shall measure the total (both a.c. and d.c. components) RMS current.		Р
	As indicated in Table 30 for different inverter types, array types, and inverter isolation levels, detection may be required for excessive continuous residual current, excessive sudden changes in residual current, or both, according to the following limits:		P
	 a) Continuous residual current: The inverter shall disconnect within 0,3 s and indicate a fault in accordance with 13.9 if the continuous residual current exceeds: 		
	 maximum 300 mA for inverters with continuous ouput power rating ≤30kV; 		N/A
	 maximum 10 mA per kVA of rated continuous output power for inverters with continuous output power rating > 30 kVA. 		Р
	The inverter may attempt to re-connect if the array insulation resistance meets the limit in 4.8.2.		Р
	b) Sudden changes in residual current: The inverter shall disconnect from the mains within the time specified in Table 31		Р
	The inverter indicates a fault in accordance with 13.9, if a sudden increase in the RMS residual current is detected exceeding the value in the table.		Р
	- 30mA@0,3s		Р
	- 60mA@0,15s		Р
	- 150mA@0,04s		Р
	The inverter may attempt to re-connect if the array insulation resistance meets the limit in 4.8.2.		Р
4.8.3.5.2	Test for detection of excessive continuous residual current: test repeated 5 times and	See appended test table 4.8.3.5.2 Test for detection of	Р

Page 12 of 28 Report No. 083-52008203-000 part 2 of 2

	IEC 62109-2		
Clause	Requirement + Test	Result - Remark	Verdic
	time to disconnect shall not exceed 0,3 s.	excessive continuous residual current	
4.8.3.5.3	Test for detection of sudden changes in residual current repeated 5 times and each of the 5 results shall not exceed the time limit indicated in for each row (30mA, 60mA and150mA) of Table 31.	See appended test table 4.8.3.5.3 Test for detection of sudden changes in residual current	Ρ
4.8.3.6	Systems located in closed electrical operating areas	Based on risk analysis, area between inverter side of that isolation transformer and mains shall be protected as systems located in closed electrical operating areas, indicating which forms of shock hazard protection are and are not provided integral to the inverter in installation instructions. All operation, installation and maintenance shall be followed with HUAWEI instruction strictly.	Ρ
	The protection against shock hazard is not required if the installation information provided with the inverter indicates the restriction for use in a closed electrical operating area, and		Р
	Installation information indicates what forms of shock hazard protection are and are not provided integral to the inverter, in accordance with 5.3.2.7.		Р
	The inverter shall be marked as in 5.2.2.6.		Р
5	MARKING AND DOCUMENTATION		Р
5.1	Marking		P
5.1.4	Equipment ratings		P
5.1.4	PV input ratings:	All applicable parameters refer to marking plate	P
	- Vmax PV (absolute maximum) (d.c. V)		Р
	- Isc PV (absolute maximum) (d.c. A)		Р
	a.c. output ratings:		Р
	- Voltage (nominal or range) (a.c. V)		Р
	- Current (maximum continuous) (a.c. A)		P
	- Frequency (nominal or range) (Hz)		P
	- Power (maximum continuous) (W or VA)		P
	- Power factor range		P
	a.c input ratings:		N/A
	- Voltage (nominal or range) (a.c. V)		N/A
	- Current (maximum continuous) (a.c. A)		N/A
	- Frequency (nominal or range) (Hz)		N/A
	d.c. output ratings:		N/A
	- Voltage (nominal or range) (d.c. V)		N/A
	- Current (maximum continuous) (d.c. A) Protective class (I or II or III)	Class I	N/A P

Page 13 of 28 Report No. 083-52008203-000 part 2 of 2

	IEC 62	109-2				
Clause	Requirement + Test	Result - Remark	Verdic			
	An inverter that is adjustable for more than		N/A			
	one nominal output voltage shall be marked					
	to indicate the particular voltage for which it					
	is set when shipped from the factory.					
5.2	Warning markings		Р			
5.2.2	Content for warning markings		Р			
5.2.2.6	Inverters for closed electrical operating areas		Р			
	Where required by 4.8.3.6, an inverter not		Р			
	provided with full protection against shock					
	hazard on the PV array shall be marked with					
	a warning that the inverter is only for use in a					
	closed electrical operating area, and referring to the installation instructions.					
5.3	Documentation		Р			
5.3.2	Information related to installation		P			
5.3.2.1	Ratings. Subclause 5.3.2 of Part 1 requires the	e documentation to include ratings	P			
5.0.2.1	information for each input and output. For inve		•			
	Table 33 below. Only those ratings that are applicable based on the type of					
	inverter are required.	, , , , , , , , , , , , , , , , , , , ,				
	PV input quantities:	All applicable parameters refer to	Р			
		user manual	P			
	- Vmax PV (absolute maximum) (d.c. V)					
	- PV input operating voltage range (d.c. V)					
	Maximum operating PV input current					
	(d.c. A)					
	- Isc PV (absolute maximum) (d.c. A)		P			
	- Max. inverter backfeed current to the		Р			
	array (a.c. or d.c. A)		P			
	a.c. output quantities:	All applicable parameters refer to user manual	Р			
	- Voltage (nominal or range) (a.c. V)		Р			
	- Current (maximum continuous) (a.c. A)		Р			
	- Current (inrush) (a.c. A, peak and	Max. inrush current is 21A peak	Р			
	duration)	@14ms				
	- Frequency (nominal or range) (Hz)		Р			
	- Power (maximum continuous) (W or VA)		Р			
	- Power factor range		Р			
	- Maximum output fault current (a.c. A,	Max. fault current is 207,3A peak	Р			
	peak and duration or RMS)	@5,2ms				
	- Maximum output overcurrent protection		Р			
	(a.c. A)		N/A			
	a.c. input quantities: - Voltage (nominal or range) (a.c. V)		N/A			
			N/A			
	 Current (maximum continuous) (a.c. A) Current (inrush) (a.c. A, peak and 		N/A N/A			
	duration)		11/7			
	- Frequency (nominal or range) (Hz)		N/A			
	d.c input (other than PV) quantities:		N/A			
	- Voltage (nominal or range) (d.c. V)		N/A			
	- Nominal battery voltage (d.c. V)		N/A			
	- Current (maximum continuous) (d.c. A)		N/A			

Page 14 of 28 Report No. 083-52008203-000 part 2 of 2

	IEC 62	109-2				
Clause	Requirement + Test	Result - Remark	Verdict			
	d.c. output quantities:		N/A			
	- Voltage (nominal or range) (d.c. V)		N/A			
	- Nominal battery voltage (d.c. V)		N/A			
	- Current (maximum continuous) (d.c. A)		N/A			
	Protective class (I or II or III)	Class I	Р			
	Ingress protection (IP) rating per part 1	IP66	Р			
5.3.2.2	Grid-interactive inverter setpoints		Р			
	 For a grid-interactive unit with field adjustable trip points, trip times, or reconnect times, the presence of such controls, the means for adjustment, the factory default values, and the limits of the ranges of adjustability shall be provided in the documentation for the PCE or in other format such as on a website. Provided solution: The setting of field adjustable setpoints shall be accessible from the PCE 	Refer to user manual Special software via communication with password	P			
		protected				
5.3.2.3	Transformers and isolation		Р			
	provided, and if so, what level of insulation (functional, basic, reinforced, or double) is provided by that transformer. The instructions shall also indicate what the resulting installation requirements are regarding such things as earthing or not earthing the array, providing external residual current detection devices, etc.		N/A			
	An inverter shall be provided with information to the installer regarding:					
	- providing of internal isolation transformer		N/A			
	- the level of insulation (functional, basic, reinforced, or double)		N/A			
	The instructions shall also indicate what the rearring:	sulting installation requirements are	Р			
	- earthing or not earthing the array	Unearthed array	Р			
	providing external residual current detection devices	Pls. follow national regulations	Р			
	 requiring an external isolation transformer, 		Р			
5.3.2.4	Transformers required but not provided	Required, pls. refer to HUAWEI technical information about transformer	Р			
	An inverter that requires an external isolation transformer not provided with the unit, shall be provided with instructions that specify, and for the external isolation transformer with which it is intended to be used:					
	- the configuration type		Р			
	- electrical ratings		Р			
	- environmental ratings					
5.3.2.5	PV modules for non-isolated inverters		Р			

Page 15 of 28 Report No. 083-52008203-000 part 2 of 2

IEC 62109-2						
Clause	Requirement + Test	Result - Remark	Verdic			
	Non-isolated inverters shall be provided with installation instructions that require PV modules that have an IEC 61730 Class A ratingIEC 61730 Class A rating required					
	If the maximum AC mains operating voltage is higher than the PV array maximum system voltage then the instructions shall require PV modules that have a maximum system voltage rating based upon the AC mains		N/A			
5.3.2.6	voltage. Non-sinusoidal output waveform information		N/A			
5.5.2.0	The instruction manual for a stand-alone invert include a warning that:	er not complying with 4.7.5.2 shall	N/A			
	- the waveform is not sinusoidal,		N/A			
	- some loads may experience increased heating,		N/A			
	 the user should consult the manufacturers of the intended load equipment before operating that load with the inverter 		N/A			
	The inverter manufacturer shall provide inform	ation regarding:	N/A			
	- what types of loads may experience increased heating		N/A			
	 recommendations for maximum operating times with such loads 		N/A			
	The inverter manufacturer shall specify for the waveforms as determined by the testing in 4.7.5.3.2 through 4.7.5.3.4.:					
	- THD		N/A			
	- slope		N/A			
	- peak voltage		N/A			
5.3.2.7	Systems located in closed electrical operating areas		N/A N/A			
	Where required by 4.8.3.6, an inverter not provided with full protection against shock hazard on the PV array shall be provided with installation instructions:					
	 requiring that the inverter and the array must be installed in closed electrical operating areas 		N/A			
	 indicating which forms of shock hazard protection are and are not provided integral to the inverter (for example the RCD, isolation transformer complying with the 30 mA touch current limit, or residual current monitoring for sudden changes) 		N/A			
5.3.2.8	Stand-alone inverter output circuit bonding		N/A			
-	Where required by 7.3.10, the documentation f following:	or an inverter shall include the	N/A			

Page 16 of 28 Report No. 083-52008203-000 part 2 of 2

IEC 62109-2						
Clause	Requirement + Test	Result - Remark	Verdict			
	 if output circuit bonding is required but is not provided integral to the inverter, the required means shall be described in the installation instructions, including which conductor is to be bonded and the required current carrying capability or cross-section of the bonding means; 					
	 if the output circuit is intended to be floating, the documentation for the inverter shall indicate that the output is floating. 		N/A			
5.3.2.9	Protection by application of RCD's		N/A			
	Where the requirement for additional protection in 4.8.3.1 is met by requiring an RCD that is not provided integral to the inverter, as allowed by 4.8.3.4, the installation instructions shall state the need for the RCD,.	If required strictly, should be type B	N/A			
	and shall specify its rating, type, and required circuit location		N/A			
5.3.2.10	Remote indication of faults		Р			
	The installation instructions shall include an explanation of how to properly make connections to (where applicable), and use, the electrical or electronic fault indication required by 13.9.	Refer to user manual of SUN2000 APP	Ρ			
5.3.2.11	External array insulation resistance measurement and response	IRM function integrated in inverter	N/A			
	The installation instructions for an inverter for use with ungrounded arrays that does not incorporate all the aspects of the insulation resistance measurement and response requirements in 4.8.2.1, must include:					
	- for isolated inverters: an explanation of what aspects of array insulation resistance measurement and response are not provided, and		N/A			
	 an instruction to consult local regulations to determine if any additional functions are required or not; 		N/A			
	 for non-isolated inverters: an explanation of what external equipment must be provided in the system, and 		N/A			
	 what the setpoints and response implemented by that equipment must be, and: 		N/A			
	 how that equipment is to be interfaced with the rest of the system. 		N/A			
5.3.2.12	Array functional grounding information	Not functional ground array used	N/A			
	Where approach a) of 4.8.2.2 is used, the installation instructions for the inverter shall include all of the following:					
	 a) the value of the total resistance between the PV circuit and ground integral to the inverter 		N/A			

Page 17 of 28 Report No. 083-52008203-000 part 2 of 2

	IEC 62	109-2				
Clause	Requirement + Test	Result - Remark	Verdict			
	 b) the minimum array insulation resistance to ground that system designer or installer must meet when selecting the PV panel and system design, based on the minimum value that the design of the PV functional grounding in the inverter was based on; 					
	 c) the minimum value of the total resistance R = VMAX PV/30 mA that the system must meet, with an explanation of how to calculate the total		N/A			
	d) a warning that there is a risk of shock hazard if the total minimum resistance requirement is not met.		N/A			
5.3.2.13	Stand-alone inverters for dedicated loads					
	Where the approach of 4.7.5.5 is used, the installation instructions for the inverter shall include a warning that the inverter is only to be used with the dedicated load for which it was evaluated, and		N/A			
	shall specify the dedicated load.		N/A			
5.3.2.14	Identification of firmware version(s)		Р			
	An inverter utilizing firmware for any protective functions shall provide means to identify the firmware version.	V001R001	Р			
	This can be a marking, but the information can also be provided by a display panel, communications port or any other type of user interface	Refer to user manual of SUN2000 APP	P			
7	PROTECTION AGAINST ELECTRIC SHOCK		N/A			
7.3	Protection against electric shock		N/A			
7.3.10	Additional requirements for stand-alone inverte	re	N/A			
7.3.10	One circuit conductor bonded to earth to create a grounded conductor and an earthed system.		N/A N/A			
	The means used to bond the grounded conductor to protective earth provided within the inverter or		N/A			
	as part of the installation		N/A			
	If not provided integral to the inverter, the required means shall be described in the installation instructions as per 5.3.2.8.		N/A			
	The means used to bond the grounded conductor to protective earth shall comply with the requirements for protective bonding in Part 1,		N/A			

Page 18 of 28 Report No. 083-52008203-000 part 2 of 2

	IEC 62	109-2					
Clause	Requirement + Test	Result - Remark	Verdict				
	If the bond can only ever carry fault currents in stand-alone mode, the maximum current for the bond is determined by the inverter maximum output fault current.						
	Output circuit bonding arrangements shall ensure that in any mode of operation, the system only has the grounded circuit conductor bonded to earth in one place at a time.		N/A				
	Switching arrangements may be used, in which case the switching device used is to be subjected to the bond impedance test along with the rest of the bonding path		N/A				
	Inverters intended to have a circuit conductor bonded to earth shall not impose any normal current on the bond except for leakage current.		N/A				
	Outputs that are intentionally floating with no circuit conductor bonded to ground, must not have any voltages with respect to ground that are a shock hazard in accordance with Clause 7 of Parts 1 and 2.		N/A				
	The documentation for the inverter shall indicate that the output is floating as per 5.3.2.8.		N/A				
7.3.11	Functionally grounded arrays		N/A				
	All PV conductors in a functionally grounded array shall be treated as being live parts with respect to protection against electric shock.		N/A				
9	PROTECTION AGAINST FIRE HAZARDS						
9.3							
9.3.4	Short-circuit and overcurrent protection		P P				
9.3.4	Inverter backfeed current onto the array The backfeed current testing and documentation requirements in Part 1 apply, including but not limited to the following.						
	Inverter backfeed current onto the PV array maximum value	Maximum inverter backfeed current form grid to the array is 0A based on test/circuit topology analysis.	Р				
	This inverter backfeed current value shall be provided in the installation instructions regardless of the value of the current, in accordance with Table 33.	Refer to user manual	Ρ				
13	PHYSICAL REQUIREMENTS		P				
13.9			P P				
13.3	Fault indication Where this Part 2 requires the inverter to indicate a fault, both of the following shall be provided:						
	a) a visible or audible indication, integral to the inverter, and detectable from outside the inverter, and	Visible indication	Р				

Page 19 of 28 Report No. 083-52008203-000 part 2 of 2

	IEC 62109-2						
Clause	Requirement + Test	Result - Remark	Verdict				
	b) an electrical or electronic indication that can be remotely accessed and used.	Communication method for remote accessed and used.	Р				
	The installation instructions shall include information regarding how to properly make connections (where applicable) and use the electrical or electronic means in b) above, in accordance with 5.3.2.10.	Refer to user manual of SUN2000 APP	Ρ				

Page 20 of 28 Report No. 083-52008203-000 part 2 of 2

4.4.4	TABLE: Single fault condition to be applied							
	Ambient ten	nperature (°C	;)		N/A(at the prevailing ambient temperature)		—	
		ce for EUT: N output rating		,	DC source: Keysight, N8957APV,			
					AC sour RS90, 9	ce: California Instruments, 0kW		
4.4.4.15.1	Fault-tolerar	Fault-tolerance of residual current monitoring						
Component No.	Fault	Supply voltage (V)	Test time	Fuse #	Fuse current (A)	Observation		
						FID: EUT could not star up. A in open.	AC relays	
Q601, C-E	SC before	600Vdc/	10 min	-	30	SD: I Yes /□ No, GD: I Ye		
,	start-up	480Vac				RO: I Yes /□ No, NCD: IN No		
						NH: I Yes /□ No, DST: I Y FID: EUT normal work.	′es /□ No	
						SD: I Yes / No, GD: I Yes	s /□ No	
Q601, C-E	SC	600Vdc/ 480Vac	10 min	-	30	RO: I Yes /□ No, NCD: IN No		
						NH: ⊠ Yes /□ No, DST: ⊠ Y	′es /⊟ No	
						FID: EUT shut down with fau indication.		
C501	sc	600Vdc/	10 min		30	SD: 🗷 Yes / 🗆 No, GD: 🗷 Ye	es /⊟ No	
(+5V power)	50	480Vac	10 min	-	30	RO: I Yes /□ No, NCD: IN No	∕es /□	
						NH: ⊠ Yes /□ No, DST: ⊠ Y		
						FID: EUT could not star up w indication. AC relays in open		
R626	OC before start-up	600Vdc/ 480Vac	10 min	-	30	SD: X Yes / No, GD: X Yes		
	start-up	400 vac				RO: I Yes /□ No, NCD: IN No	res /⊔	
						NH: Yes / No, DST: Y		
						FID: EUT work normally with indication.	out fault	
R626	ос	600Vdc/	10 min	_	30	SD: 🗆 Yes /🗷 No, GD: 🗆 Ye	s ∕⊠ No	
11020	00	480Vac	10 11111		50	RO: ⊠ Yes /□ No, NCD: ⊠ No	∕es /□	
						NH: ⊠ Yes /□ No, DST: ⊠ Y		
						FID: EUT could not star up w indication. AC relays in open		
D / //-	OC before	600Vdc/				SD: I Yes /□ No, GD: I Ye		
R1427	start-up	480Vac	10 min	-	30	RO: ⊠ Yes /□ No, NCD: ⊠ No		
						NH: I Yes /□ No, DST: I Y	′es /□ No	

Page 21 of 28 Report No. 083-52008203-000 part 2 of 2

						FID: EUT shut down with fault indication. AC relays in open.
R1427	ос	600Vdc/	10 min		30	SD: 🗆 Yes /🗷 No, GD: 🗆 Yes /🗷 No
K1427	00	480Vac	10 11111	-	30	RO: ⊠ Yes /□ No, NCD: ⊠ Yes /□ No
						NH: I Yes /□ No, DST: I Yes /□ No
						FID: EUT could not star up with fault indication. AC relays in open.
L601, Pin 7-8	SC before	600Vdc/ 480Vac	10 min	-	20	SD: ⊠ Yes /□ No, GD: ⊠ Yes /□ No
LOUT, PIIT 7-0	start-up				30	RO: ⊠ Yes /□ No, NCD: ⊠ Yes /□ No
						NH: ⊠ Yes /□ No, DST: ⊠ Yes /□ No
						FID: EUT shut down with fault indication. AC relays in open.
L601, Pin 7-8	80	600Vdc/	10		30	SD: □ Yes /⊠ No, GD: □ Yes /⊠ No
LOUT, FIIT 7-0	30	480Vac	10 min	-	30	RO: I Yes /□ No, NCD: I Yes /□
						No
				NH: I Yes /□ No, DST: I Yes /□ No		
Check that the	residual curr	ent monitorin	rly	Yes		
Supplementary	y information:	also see IEC	62109-1	report		

4.4.4	TABLE: Sing	gle fault cond	ition to be	e applied			Р
	Ambient terr	nperature (°C))		. N/A(at the prevailing ambient temperature)		—
	Power source for EUT: Manufacturer, model/type, output rating				1500V, 3	ce: California Instruments,	
4.4.4.15.2	Fault-tolerance of automatic disconnecting means						
Component No.	Fault	Supply voltage (V)	Test time	Fuse #	Fuse current (A)	Observation	
AC relay K504/K503, contacts	SC before start-up	600Vdc/ 480Vac	10 min	-	30	FID: EUT could not start up. Start-up again with same result. SD: ☑ Yes /□ No, GD: ☑ Yes /□ No RO: ☑ Yes /□ No, NCD: ☑ Yes /□ No NH: ☑ Yes /□ No, DST: ☑ Yes /□ N	
C503	SC before start-up	600Vdc/ 480Vac	10 min	-	30	FID: EUT could not start up. AC relays in open. SD: I Yes /□ No, GD: I Yes /□ No RO: I Yes /□ No, NCD: I Yes /□ No NH: I Yes /□ No, DST: I Yes /□ N	

Page 22 of 28 Report No. 083-52008203-000 part 2 of 2

R540	SC before start-up	600Vdc/ 480Vac	10 min	-	30	 FID: EUT could not start up.AC relays in open. SD: ☑ Yes /□ No, GD: ☑ Yes /□ No RO: ☑ Yes /□ No, NCD: ☑ Yes /□ No NH: ☑ Yes /□ No, DST: ☑ Yes /□ No FID: EUT shut down. SD: ☑ Yes /□ No, GD: ☑ Yes /□ No
R540	SC	600Vdc/ 480Vac	10 min	-	30	RO: ☑ Yes /□ No, NCD: ☑ Yes /□ No NH: ☑ Yes /□ No, DST: ☑ Yes /□ No
Q505, C-E	OC before start-up	600Vdc/ 480Vac	10 min	-	30	FID: EUT shut down after start up with fault indication. SD: ☑ Yes /□ No, GD: ☑ Yes /□ No RO: ☑ Yes /□ No, NCD: ☑ Yes /□ No NH: ☑ Yes /□ No, DST: ☑ Yes /□ No
Q505, B-E	OC before start-up	600Vdc/ 480Vac	10 min	-	30	FID: EUT shut down after start up with fault indication. SD: ☑ Yes /□ No, GD: ☑ Yes /□ No RO: ☑ Yes /□ No, NCD: ☑ Yes /□ No NH: ☑ Yes /□ No, DST: ☑ Yes /□ No
R539	OC before start-up	600Vdc/ 480Vac	10 min	-	30	FID: EUT could not start up. SD: ☑ Yes /□ No, GD: ☑ Yes /□ No RO: ☑ Yes /□ No, NCD: ☑ Yes /□ No NH: ☑ Yes /□ No, DST: ☑ Yes /□ No
U3, +3,3V - GND	SC	600Vdc/ 480Vac	10min	-	30	FID: EUT shut down. SD: ☑ Yes /□ No, GD: ☑ Yes /□ No RO: ☑ Yes /□ No, NCD: ☑ Yes /□ No NH: ☑ Yes /□ No, DST: ☑ Yes /□ No
U33, +3,3V - GND	SC	600Vdc/ 480Vac	10min	-	30	FID: EUT shut down. SD: ☑ Yes /□ No, GD: ☑ Yes /□ No RO: ☑ Yes /□ No, NCD: ☑ Yes /□ No NH: ☑ Yes /□ No, DST: ☑ Yes /□ No
Check that the separation bas				Yes		
Each active ph	nase can be s	witched. (L a	nd N)			Yes
Supplementar	y information:	also see IEC	62109-1	report		

4.4.4.17	TABLE: Cooling system failure – Blanketing test (SUN2000-42KTL-M3)					
	Test voltage (V)	720VDC/480VAC				
Max. temperatu	ire T of part/at:	T (°C)	Permitted 7	T (°C)		
Ambient Tempe	erature	25,0	-			
CPU (U3)		60,8	-			

Page 23 of 28 Report No. 083-52008203-000 part 2 of 2

CPLD (U0701)	79,5	_
L4 FLASH (U203)	61,2	-
Bus ground power arrester (F301)	66,8	
Bus ground power arrester (F306)	62,1	-
Gas Discharge Tube (D306)	68,1	
Y Cap (C422)	64,9	-
X Cap (C606)	58,4	
Current Hall (U401)	65,7	
Grid relay coil(K503)	67,1	-
EMI Com-mode Inductor (T601)	65,2	-
Power Arrester (F607)	58,5	-
Power Arrester (F604)	59,3	
Gas Discharge Tube (D601)	60,1	-
High Frequency Transformer (T701)	67,1	-
DC Aux Transformer MOS (Q702)		
THT Film Capacitor (C710)	82,5	-
	63,0	-
Driver Transformer (T801)	65,1	-
High Frequency Transformer (T1101) MOSFET (Q1101)	69,6	-
	65,2	-
THT Film Capacitor (C1104)	67,2	-
AC AUX source PWM Controller (U1204)	68,5	-
Driver Transformer (T1201)	65,0	-
Optical Coupler (U1904)	75,8	-
Electromagnetic Relay (K1001)	66,3	-
Gas Discharge Tube (D2309)	60,9	-
THT Film Capacitor (C1104)	67,2	-
HF Current Transformer (T302)	65,3	-
THT Film Capacitor (C302)	64,6	-
Power Arrester (F301)	66,8	-
Discharge Tube (D309)	68,1	-
Semiconductor Sensor (U908)	67,3	-
Electromagnetic Relay (K1001)	67,1	-
THT Film Capacitor (C1429)	66,1	-
Driver Transformer (T2101)	70,8	-
PV terminal	62,1	90
AC terminal	28,8	90
Rotary Switch	61,8	90
Enclosure (front)	48,6	90
Enclosure (side)	53,9	90
Enclosure (top)	49,7	90
Heat-sink	67,3	90
Mounting Surface	28,9	90
Handle	70,9	90
DC switch internal	59,6	90
DC switch knob	55,4	90
MPPT wire	61,3	-
BST Inductor wire	52,5	-
INV Inductor wire	72,4	-
BST Inductor	66,5	-
INV Inductor	75,7	-
AC internal wire Supplementary information:	47,5	-

Page 24 of 28 Report No. 083-52008203-000 part 2 of 2

2. No over temperature observed in components, no other hazard observed.
3. The duration of the test less than 7 h for the temperatures stabilize and no external surface of the inverter is at a temperature exceeding 90 °C.

4.4.4.17	TABLE: Cooling system failure – B		
	Test voltage (V)		OVAC
Max. temperat	ture T of part/at:	T (°C)	Permitted T (°C)
Ambient Temp	perature	25,0	-
CPU (U3)		98,0	-
CPLD (U0701))	111,2	-
L4 FLASH (U2		76,8	-
Bus ground po	ower arrester (F301)	82,4	-
	ower arrester (F306)	82,4	-
Gas Discharge	e Tube (D306)	79,6	-
Y Cap (C422)	· · ·	80,5	-
X Cap (C606)		71,9	-
Current Hall (L	J401)	80,5	-
Grid relay coil	(K503)	80,5	-
	le Inductor (T601)	75,9	-
Power Arreste	r (F607)	73,5	-
Power Arreste		73,2	-
Gas Discharge	e Tube (D601)	74,1	-
High Frequence	cy Transformer (T701)	82,5	-
	former MOS (Q702)	100,7	-
THT Film Cap	acitor (C710)	71,5	-
Driver Transfo	rmer (T801)	79,7	-
	cy Transformer (T1101)	81,7	-
MOSFET (Q1	101)	105,3	-
THT Film Cap		78,6	-
	e PWM Controller (U1204)	82,0	-
Driver Transfo		78,6	-
Optical Couple	· · · · ·	81,4	-
	tic Relay (K1001)	80,4	-
	e Tube (D2309)	76,2	-
THT Film Cap		78,6	-
	ansformer (T302)	80,4	-
THT Film Cap		81,4	-
Power Arreste		82,4	-
Discharge Tub	pe (D309)	81,4	-
	r Sensor (U908)	77,9	-
	tic Relay (K1001)	80,4	-
	acitor (C1429)	83,5	-
Driver Transfo		84,8	-
PV terminal	, , ,	52,8	90
AC terminal		68,2	90
Rotary Switch		77,5	90
Enclosure (fro		22,4	90
Enclosure (sid		66,7	90
Enclosure (top	/	68,2	90
Heat-sink	*	75,3	90
Mounting Surf	ace	41,2	90
Handle		69,7	90

Page 25 of 28 Report No. 083-52008203-000 part 2 of 2

77,5	90
60,0	90
72,3	-
78,5	-
80,0	-
70,0	-
107,0	-
66,9	-
	60,0 72,3 78,5 80,0 70,0 107,0

Supplementary information: 1. Lowest full load MPP voltage with max. power output @25°C (Blanketing test).

2. No over temperature observed in components, no other hazard observed.

3. The duration of the test less than 7 h for the temperatures stabilize and no external surface of the inverter is at a temperature exceeding 90 °C.

4.8.2		: Array insulation res	istance detection for ir	nverters for ungrounde	ed and	Р
4.8.2.1	Array i	nsulation resistance of	detection for inverters	for ungrounded arrays	;	Р
DC Voltage minimum op voltage (V)	erating	DC Voltage for inverter begin operation (V)	Resistance between ground and PV input terminal (kΩ)	Required Insulation resistance R = (VMAX PV / 30mA) (kΩ)	Re	sult
ISO setting=	36,7kΩ		PV1+ to earth			
180		200	33	36,7	Insulat	on fault
180		200	33	36,7		on fault
180		200	33	36,7	Insulat	on fault
180		200	33	36,7	Insulat	on fault
180		200	33	36,7	Insulati	on fault
180		200	40	36,7	Normal (Operation
180		200	40	36,7	Normal (Operation
180		200	40	36,7	Normal (Operation
180		200	40	36,7	Normal (Operation
180		200	40	36,7	Normal (Operation
			PV1- to earth			
180		200	33	36,7	Insulati	on fault
180		200	33	36,7	Insulati	on fault
180		200	33	36,7	Insulati	on fault
180		200	33	36,7	Insulat	on fault
180		200	33	36,7	Insulat	on fault
180		200	40	36,7	Normal (Operation
180		200	40	36,7	Normal (Operation
180		200	40	36,7	Normal (Operation

Page 26 of 28 Report No. 083-52008203-000 part 2 of 2

180	200	40	36,7	Normal Operation
180	200	40	36,7	Normal Operation

Note:

For isolated inverters, shall indicate a fault in accordance with 13.9 (operation is allowed); the fault indication shall be maintained until the array insulation resistance has recovered to a value higher than the limit above

For non-isolated inverters, or inverters with isolation not complying with the leakage current limits in the minimum inverter isolation requirements in Table 30, shall indicate a fault in accordance with 13.9, and shall not connect to the mains; the inverter may continue to make the measurement, may stop indicating a fault and may connect to the mains if the array insulation resistance has recovered to a value higher than the limit above.

It is not required to test all PV input terminals if analysis of the design indicates that one or more terminals can be expected to have the same result, for example where multiple PV string inputs are in parallel.

Supplementary information:

Additional test with first one pole ground fault, following an insulation resistance below limit simulated, then allow the inverter to start, the inverter shall not connect to the mains.

Test co	onditions:	Output power (kVA): 42 Input voltage (VDC): 85 Frequency (Hz): 50		
		Output AC Voltage (VA		
4.8.3.5.2	Test for detection	on of excessive continuou	us residual current	Р
	Fault Curre	nt (mA)	Disconnection time (ms)	
Measured Fault Curre		Limit output power ≤ 30 kVA kVA for output power > 30 kVA	Measured Disconnection time	Limit
)mA@300ms	
			to earth:	
413		420	242	300
409		420	230	300
421		420	231	300
416		420	230	300
407		420	231	300
		PV1- t	o earth:	
411		420	244	300
418		420	241	300
423		420	240	300
415		420	241	300
414		420	245	300

Note:

- maximum 300mA for inverters with continuous output power rating ≤30 kVA;

 maximum 10mA per kVA of rated continuous output power for inverters with continuous output power rating > 30 kVA.

This test shall be repeated 5 times, and for all 5 tests the time to disconnect shall not exceed 0,3s. The test is repeated for each PV input terminal. It is not required to test all PV input terminals if analysis of the design indicates that one or more terminals can be expected to have the same result, for example

Page 27 of 28 Report No. 083-52008203-000 part 2 of 2

where multiple PV string inputs are in parallel.

4.8.3.5.3	TABLE: Test for detection of sudden changes in residual current(only for RCM function)	Р
	PV1+ to earth:	
L :	UN	Lineit (m. s
Limit (mA)	Disconnection time (ms)	Limit (ms
	Default: 30mA@300ms	
30	265	300
30	262	300
30	266	300
30	263	300
30	263	300
	Default: 60mA@150ms	
60	125	150
60	133	150
60	126	150
60	132	150
60	121	150
	Default: 150mA@40ms	
150	32	40
150	31	40
150	28	40
150	35	40
150	38	40
	PV1- to earth	
Line it (no A)	PV1- to earth UN	
Limit (mA)		— Limit (ms
Limit (mA)	UN	Limit (ms
Limit (mA) 30	UN Disconnection time (ms)	Limit (ms
	UN Disconnection time (ms) Default: 30mA@300ms	
30	UN Disconnection time (ms) Default: 30mA@300ms 263	300
30 30	UN Disconnection time (ms) Default: 30mA@300ms 263 262	<u>300</u> 300
30 30 30	UN Disconnection time (ms) Default: 30mA@300ms 263 262 263	300 300 300
30 30 30	UN Disconnection time (ms) Default: 30mA@300ms 263 262 263 263 263 263 263 263 263	300 300 300
30 30 30 30 30	UN Disconnection time (ms) Default: 30mA@300ms 263 262 263 263 263 263 263 263 263 263 263 264	300 300 300 300 300
30 30 30 30 30 30	UN Disconnection time (ms) Default: 30mA@300ms 263 262 263 263 266 266 264 Default: 60mA@150ms	300 300 300 300 300 300
30 30 30 30 30 30 60	UN Disconnection time (ms) Default: 30mA@300ms 263 262 263 263 264 Default: 60mA@150ms 128	300 300 300 300 300 300 150
30 30 30 30 30 30 60 60	UN Disconnection time (ms) Default: 30mA@300ms 263 262 263 263 263 264 Default: 60mA@150ms 128 126	300 300 300 300 300 300 150 150
30 30 30 30 30 30 60 60 60	UN Disconnection time (ms) Default: 30mA@300ms 263 262 263 263 266 264 Default: 60mA@150ms 128 128	300 300 300 300 300 300 150 150 150
30 30 30 30 30 30 60 60 60 60 60	UN Disconnection time (ms) Default: 30mA@300ms 263 262 263 263 266 264 Default: 60mA@150ms 128 128 128 128 128	300 300 300 300 300 150 150 150 150
30 30 30 30 30 30 60 60 60 60 60	UN Disconnection time (ms) Default: 30mA@300ms 263 262 263 263 266 264 Default: 60mA@150ms 128 118	300 300 300 300 300 150 150 150 150
30 30 30 30 30 30 60 60 60 60 60 60	UN Disconnection time (ms) Default: 30mA@300ms 263 262 263 266 264 Default: 60mA@150ms 128 118 Default: 150mA@40ms	300 300 300 300 300 150 150 150 150
30 30 30 30 30 30 60 60 60 60 60 60 60 150	UN Disconnection time (ms) Default: 30mA@300ms 263 262 263 266 264 Default: 60mA@150ms 128 128 128 128 128 28	300 300 300 300 300 300 300 150 150 150 150 150 40
30 30 30 30 30 30 60 60 60 60 60 60 60 60 150	UN Disconnection time (ms) Default: 30mA@300ms 263 262 263 266 264 Default: 60mA@150ms 128 128 128 128 128 263 264 264 Default: 60mA@150ms 128 128 263 264 264 264 264 265 264 265 28 28	300 300 300 300 300 150 150 150 150 150 150 40 40

Test condition: Ic + 30/60/150mA <= Icmax. R1 is set that 30/60/150mA Flow and switch S is closed.

Page 28 of 28 Report No. 083-52008203-000 part 2 of 2

Supplementary information: Same design on other MPP trackers, it is not required to test on other MPP trackers because analysis of the design indicates that other MPP trackers expected to have the same result.

.....End of test report.....